

init_11Nov04

Successfully removed tdl files

Successfully started file /tmp/hardware.tdl

T6683 system initialization start at 2004/11/12 07:09:05

----- SYSTEM CONFIGURATION INFORMATION -----

PIN CONFIGURATION

MAINFRAME : 1 - 1024
TH1 [CTD] : 1 - 1024

DPU CONFIGURATION

DPS CH : 1 - 32
UDC CH : 1 - 8
PPP CH : 1 - 1024
PEP CH : 1 - 64
DPS PSRR : OFF
LINE FREQUENCY : 60Hz
DMM MODEL : R6581T

SQPG CONFIGURATION

MAX VGC : 4MW
MAX CTB : 64MW
MAX TTb : 64MW

OPTIONS CONFIGURATIONS : HRS ON SCPG ON ALPG OFF MIXED ON MIXED2 OFF

OPTION PG

MAX SCPG : 4Gbit

HCDPS

HCDPS CH : 65 - 80

MIXED OPTION

HSC OPTION : 1 - 4

HI-FIX TYPE : 1024B

DPU initialization start at 07:09:05

DPU successfully initialized.

PMU initialization start at 07:11:25

PMU successfully initialized.

SQPG initialization start at 07:18:28

SQPG successfully initialized.

FPU initialization start at 07:18:28

FPU successfully initialized.

TRIGFP initialization start at 07:19:14

TRIGFP successfully initialized.

THU initialization start at 07:19:26

THU successfully initialized.

FPUTH initialization start at 07:32:31

FPUTH successfully initialized.

TRIGTH initialization start at 07:33:11

TRIGTH successfully initialized.

TRIGFPTH initialization start at 07:33:26

TRIGFPTH successfully initialized.

HCDPS initialization start at 07:33:32

HCDPS successfully initialized.

HSCLK initialization start at 07:34:56

HSCLK successfully initialized.

T6683 system initialization finish at 2004/11/12 07:35:02

-----> PASS

T6683 initialization data auto save start at 2004/11/12 07:35:02

DPU initialization data successfully saved.

PMU initialization data successfully saved.

SQPG initialization data successfully saved.

FPU initialization data successfully saved.

TRIGFP initialization data successfully saved.

THU initialization data successfully saved.

TRIGTH initialization data successfully saved.

HCDPS initialization data successfully saved.

HSCLK initialization data successfully saved.

T6683 initialization data auto save finish at 2004/11/12 07:36:21

init_11Nov04

-----> PASS

Successfully removed tdl files

Successfully started file /tmp/hardware.tdl

T6683 initialization data save start at 2004/11/12 07:38:33

----- SYSTEM CONFIGURATION INFORMATION -----

PIN CONFIGURATION

MAINFRAME : 1 - 1024
TH1 [CTD] : 1 - 1024

DPU CONFIGURATION

DPS CH : 1 - 32
UDC CH : 1 - 8
PPP CH : 1 - 1024
PEP CH : 1 - 64
DPS PSRR : OFF
LINE FREQUENCY : 60Hz
DMM MODEL : R6581T

SQPG CONFIGURATION

MAX VGC : 4MW
MAX CTB : 64MW
MAX TTb : 64MW

OPTIONS CONFIGURATIONS : HRS ON SCPG ON ALPG OFF MIXED ON MIXED2 OFF

OPTION PG

MAX SCPG : 4Gbit

HCDPS

HCDPS CH : 65 - 80

MIXED OPTION : DCAP

HSC OPTION : 1 - 4

HI-FIX TYPE : 1024B

DPU initialization data successfully saved.

PMU initialization data successfully saved.

SQPG initialization data successfully saved.

FPU initialization data successfully saved.

TRIGFP initialization data successfully saved.

THU initialization data successfully saved.

TRIGTH initialization data successfully saved.

HCDPS initialization data successfully saved.

HSClk initialization data successfully saved.

T6683 initialization data save finish at 2004/11/12 07:39:52

-----> PASS

Successfully removed tdl files

Successfully started file /tmp/hardware.tdl

T6683 initialization flags institution start at 2004/11/12 07:52:48

----- SYSTEM CONFIGURATION INFORMATION -----

PIN CONFIGURATION

MAINFRAME : 1 - 1024
TH1 [CTD] : 1 - 1024

DPU CONFIGURATION

DPS CH : 1 - 32
UDC CH : 1 - 8
PPP CH : 1 - 1024
PEP CH : 1 - 64
DPS PSRR : OFF
LINE FREQUENCY : 60Hz
DMM MODEL : R6581T

SQPG CONFIGURATION

MAX VGC : 4MW
MAX CTB : 64MW
MAX TTb : 64MW

OPTIONS CONFIGURATIONS : HRS ON SCPG ON ALPG OFF MIXED ON MIXED2 OFF

OPTION PG

MAX SCPG : 4Gbit

HCDPS

init_11Nov04

HCDPS CH : 65 - 80
MIXED OPTION : DCAP
HSC OPTION : 1 - 4
HI-FIX TYPE : 1024B

T6683 initialization flags institution finish at 2004/11/12 07:52:49

-----> PASS

tution start at 2004/11/12 07:52:48

----- SYSTEM CONFIGURATION INFORMATION -----

PIN CONFIGURATION

MAINFRAME : 1 - 1024
TH1 [CTD] : 1 - 1024

DPU CONFIGURATION

DPS CH : 1 - 32
UDC CH : 1 - 8
PPP CH : 1 - 1024
PEP CH : 1 - 64
DPS PSRR : OFF
LINE FREQUENCY : 60Hz
DMM MODEL : R6581T

SQPG CONFIGURATION

MAX VGC : 4MW
MAX CTB : 64MW
MAX TTB : 64MW

OPTIONS CONFIGURATIONS : HRS ON