

CONFIGURATION FILE

```
# Catalyst tester
#
# rusht is a 2 processor system
#   Processor 1: 1280 MHz sparcv9 (online)
#   Processor 2: 1280 MHz sparcv9 (online)
# PCI based system
# Terabus is present
#   Board ID      000-000-00
#   Serial number 0000000
# TCI is present
#
```

CATALYST_TH 1

BACKPLANE A

#Slot	Type	Serial #	Num	XptA	XptB	Name
2	949-658-00	0x0254b83	0	# 23	24	LFAC DUAL CC
3	000-000-00	0x0000000	0	# 21	22	EMPTY
4	949-787-50	0x027602b	0	# 19	20	VREG CC DUT34
5	879-792-01	0x02556b0	0	# 17	18	TIME CC
8	949-681-50	0x0220315	0	# 11	12	VHFAWG400 DIFF CC
9	949-658-00	0x0165c09	0	# 9	10	LFAC DUAL CC
10	949-681-50	0x0229e7a	0	# 7	8	VHFAWG400 DIFF CC
14	803-594-00	0x0003c95	0	# 25	26	UHFSRC CC
15	803-596-07	0x02a6e53	0	# 27	28	UWPORT
16	803-595-00	0x025c680	0	# 29	30	UWMM
17	803-594-00	0x02614bc	0	# 31	32	UHFSRC CC
18	803-596-07	0x0221fdb	0	# 33	34	UWPORT
19	803-594-00	0x0261640	0	# 35	36	UHFSRC CC
20	803-594-00	0x0261497	0	# 37	38	UHFSRC CC
21	949-681-50	0x026dd0c	0	# 39	40	VHFAWG400 DIFF CC
23	879-906-51	0x02294c9	0	# 43	44	VHFCW CC
1	949-669-00	0x026a3d0	0	# 3	0	HAS (Left HAS LA669-00)
13	949-668-00	0x027e6b5	0	# 0	0	CATALYST HAC
12	949-667-00	0x026a4ee	0	# 0	0	DIF
24	949-669-01	0x026a16d	0	# 4	0	HAS (Right HAS LA669-01)

END

RF_PIPES 1

#	pipe-name	slot	schan	description
	Z8A	15	1	# UWPORT (OSP)
	Z7A	15	2	# UWPORT (OSP)
	Z6A	15	3	# UWPORT (OSP)
	Z5A	15	4	# UWPORT (OSP)
	Z4A	18	1	# UWPORT (OSP)
	Z3A	18	2	# UWPORT (OSP)
	Z2A	18	3	# UWPORT (OSP)

Z1A	18	4	# UWPORT (OSP)
Y15B	7	1	# VHFAWG1200 CC (OSSP)
Y17B	7	2	# VHFAWG1200 CC (OSSP)
Y16B	7	3	# VHFAWG1200 CC (OSSP)
Y18B	7	4	# VHFAWG1200 CC (OSSP)
Z7C	15	1	# UWPORT (OSP)
Z6C	15	2	# UWPORT (OSP)
Z9C	15	5	# UWPORT (OSP)
Z8C	15	6	# UWPORT (OSP)
Z3C	18	1	# UWPORT (OSP)
Z2C	18	2	# UWPORT (OSP)
Z5C	18	5	# UWPORT (OSP)
Z4C	18	6	# UWPORT (OSP)
Z7D	15	3	# UWPORT (OSP)
Z6D	15	4	# UWPORT (OSP)
Z9D	15	7	# UWPORT (OSP)
Z8D	15	8	# UWPORT (OSP)
Z3D	18	3	# UWPORT (OSP)
Z2D	18	4	# UWPORT (OSP)
Z5D	18	7	# UWPORT (OSP)
Z4D	18	8	# UWPORT (OSP)

END

RF_CONNECTIONS 1

RF pipe connections between channel cards

#	slot	schan		slot	schan
	14	1	to	15	5
	16	1	to	18	7
	16	3	to	15	7
	17	1	to	15	6
	19	1	to	18	5
	20	1	to	18	6

END

#

Up to 4 Precision AC Card Cages are allowed

#

PRECISION_AC 1

#Slot	Type	Serial #	Num	Name
1	000-000-00	0x00000000	0	# EMPTY
2	000-000-00	0x00000000	0	# EMPTY
3	949-664-00	0x801433f	0	# VHFDIG MF
4	949-736-00	0x02090b1	0	# VHFAWG1200
5	949-827-00	0x11d10b6	0	# VHFAWG
6	949-320-00	0x0265250	0	# UWMS
7	949-664-00	0x800c2d5	0	# VHFDIG MF
8	949-671-01	0x802003f	0	# PACS CAGE INT

END

PRECISION_AC 2

#Slot	Type	Serial #	Num	Name
1	949-660-01	0x802581c	0	# LFACDIG

2	949-659-00	0x8022a16	0	# LFACSRC
3	949-660-01	0x80258f8	0	# LFACDIG
4	949-659-00	0x8022a80	0	# LFACSRC
5	949-827-00	0x11da183	0	# VHFAWG
6	949-320-00	0x026524e	0	# UWMS
7	949-827-00	0x11d75e4	0	# VHFAWG
8	949-671-01	0x80341c6	0	# PACS CAGE INT

END

Up to 8 Universal Backplane/Synch Power Subsystem
cages are allowed

For the Synch Power Subsystem:
Slot Type Name Instr1 # Instr2 # Ammeter #

Instr1 # - instrument connected to the first two matrix lines
Instr2 # - instrument connected to the last two matrix lines
Ammeter # - ammeter connection
to AVOID errors, put NO 0 if no instrument is connected.

#

UB_SPS_CAGE 1

# Slot	Type	Serial #	Num	Name
1	879-802-02	0x028faeb	0	# UB_SPS_802
2	517-301-00	0x01bedc2	0	# UB_APU
3	517-301-00	0x02573ac	0	# UB_APU
4	517-301-00	0x025d724	0	# UB_APU
5	517-301-00	0x01bede9	0	# UB_APU
6	517-301-00	0x024e760	0	# UB_APU
7	517-301-00	0x026ff12	0	# UB_APU
8	517-301-00	0x0259296	0	# UB_APU
9	517-301-00	0x025a93f	0	# UB_APU
10	517-301-00	0x0247641	0	# UB_APU
11	517-301-00	0x0257e11	0	# UB_APU
12	517-301-00	0x0246300	0	# UB_APU
13	517-301-00	0x02599a1	0	# UB_APU
21	879-690-00	0x028f979	0	# UB_ASY
22	517-300-01	0x025c1d1	0	# UB_TJ300

END

HSD100_CHAN_CAGE 1

#Slot	Type	Serial #	Num	Fld1	Fld2	Name
1	949-921-01	0x801071b	0			# HSD CDM 400
2	949-921-01	0x8016f70	0			# HSD CDM 400
3	949-921-01	0x8016d1b	0			# HSD CDM 400
4	949-921-01	0x801632f	0			# HSD CDM 400
5	949-820-10	0x11d09a8	0			# HSD CSB
6	949-921-01	0x80156bc	0			# HSD CDM 400
7	949-921-01	0x8016409	0			# HSD CDM 400

8	949-921-01	0x80174cb	0	# HSD CDM 400
9	949-921-01	0x8016305	0	# HSD CDM 400

END

CATALYST_TH 1
BACKPLANE B

#Slot	Type	Serial #	Num	Name
43	949-625-00	0x8020173	0	# HSD DTH
44	949-625-00	0x8017678	0	# HSD DTH
45	949-625-00	0x80200a7	0	# HSD DTH
46	949-625-00	0x8011fa3	0	# HSD DTH
47	949-626-10	0x8015623	0	# HSD THS
48	949-625-00	0x80122fd	0	# HSD DTH
49	949-625-00	0x8017709	0	# HSD DTH
50	949-625-00	0x8011faa	0	# HSD DTH
51	949-625-00	0x80111ea	0	# HSD DTH
61	949-625-00	0x8020285	0	# HSD DTH
62	949-625-00	0x800f66b	0	# HSD DTH
63	949-625-00	0x80171bb	0	# HSD DTH
64	949-625-00	0x801712f	0	# HSD DTH
65	949-626-00	0x8015c97	0	# HSD THS
66	949-625-00	0x1180556	0	# HSD DTH
67	949-625-00	0x802019f	0	# HSD DTH
68	949-625-00	0x8020249	0	# HSD DTH
69	949-625-00	0x117fda6	0	# HSD DTH

END

Trigger Switch Yard

Note: The logical slot numbers below correspond to the physical slot
numbers only for test systems which contain a TSY card
cage. In test systems which contain an SCS/TSY card cage the
mapping is:

Logical TSY slot (below)	Physical slot
1	-> 2
3	-> 1

TSY CAGE

#Slot	Type	Serial #	Num	Name
1	879-655-02	0x02518b4	0	# TSY
2	000-000-00	0xffffffff	0	# EMPTY
3	000-000-00	0xffffffff	0	# EMPTY
4	000-000-00	0xffffffff	0	# EMPTY

END

Time Subsystem

TIME_SUBSYSTEM

```
# Board ID      Serial #      Name
879-793-00      0x0252179  # TMS Timer
879-794-01      0x027D300  # TMS Counter
879-795-01      0x0274C90  # TMS Support
949-782-00      0x0276B65  # Time Mux Board 1
```

END

```
# The UWMS option resides in the PACS cage and is composed of two
# plug-in modules that may reside in one of three locations A, B or C.
# A zero in the type field indicates no plug-in module.
# PACS_UW <n> - indicates PACS cage number of these UWMS options
# Slot - indicates slot number and module position for this UWMS option
# Type - board identification number
# AWG Num - AWG instrument number connected to this UWMS option
# UHFSRC Num - UHF instrument number that has this UWMS option
# MODSRC Num - First or second MODSRC connected to above UHFSRC
```

```
#
PACS_UW 1
#
#Slot Type      Serial #  Rev    Date      AWG  UHFSRC  MODSRC  Name
#          Num      Num      Num      Num      Num
6   949-320-00  0x0265250  A      9943-0    2     1       1      # UWMS
6A  949-330-00  0x0229cf5  A      9944-0    0     0       0      # UWMS UP
CONV
6B  000-000-00  0x0000000  0      0000-0    0     0       0
6C  949-333-00  0x0265aff  C      9500-0    0     0       0      # UWMS
```

MUX

END

#

```
PACS_UW 2
#
#Slot Type      Serial #  Rev    Date      AWG  UHFSRC  MODSRC  Name
#          Num      Num      Num      Num      Num
6   949-320-00  0x026524e  A      9943-0    0     0       0      # UWMS
6A  949-330-00  0x0220495  A      9944-0    0     0       0      # UWMS UP
CONV
6B  000-000-00  0x0000000  0      0000-0    0     0       0
6C  949-333-00  0x0265162  C      9500-0    0     0       0      # UWMS
```

MUX

END

#

```
#
# The UWPORT option modules are composed of plug-in modules.
# The names list in the first column indicate the presence of
# the option modules for the uwport.
```

#

```
UWPORT_OPTIONS_HEAD 1
#
# Name Slot Type      Serial #  Rev    Num      Date
PORT  15  803-596-07  0x02a6e53  B      1      # 0424-0
NOISE 15  733-104-00  0x027fc72  A      1      # 9923-0
PORT  18  803-596-07  0x0221fdb  B      2      # 0140-0
NOISE 18  733-104-00  0x021cf97  A      2      # 9923-0
```

END

```

#
# DC Subsystem -
#
#   SRC <NUM>                [1 - 13]
#   (sources 1-5 are MATRIX sources 1-5
#   sources 6-13 are DUT sources 1-8)
#   HCU <NUM>                 *[1 - 4]
#   REF HCU <NUM>             *[1 - 4]
#   HVSRC <NUM>               *[1 - 4]
#   PWRSRC <NUM>              [1 - 4]
#   DATABITS <NUM> - <NUM>    [1 - 192]
#
# ** These instruments share the same seven-slot cage -- only one
#    instrument is allowed per slot.
#
DC_SUBSYSTEM

    HCU          5

    HCU          8
    HCU          9
    DATABITS     1 - 48

# UB_MATRIX
#
# Testhead 1
# XPTs      UB Cage  Slot      Type
# 1-4       1       2       APU
# 5-8       1       3       APU
# 9-12      1       4       APU
# 13-16     1       5       APU
# 17-20     1       6       APU
# 21-24     1       7       APU
# 25-28     1       8       APU
# 29-32     1       9       APU
# 33-36     1      10       APU
# 37-40     1      11       APU
# 41-44     1      12       APU
# 45-48     1      13       APU
END

```